PATENT NUMBER

Subclass

Class

ISSUE CLASSIFICATION

U.S. **UTILITY** Patent Application

O.I.P.E.

SCANNED ___ Q.A. ___

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/826784	D F	710	260	2181	PHAN, R

APPLICANTS

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TITLE

Processing method, chip set and controller for supporting message
signaled interrupt

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								
						☐ Continued on Issue Slip Inside File Jacket		

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)		(Date)	NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
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